

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <i>Qof</i> Q.A. <i>NA</i></p>	PATENT DATE
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APPLICATION NO. 09/965972	CONT/PRIOR	CLASS 438	SUBCLASS 5	ART UNIT 1765	EXAMINER T. S.
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APPLICANTS Sarah Kim
R. List
Bruce Block

Fabrication of 3-D capacitor with dual damascene process

TITLES

PTO-2040
12/99**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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